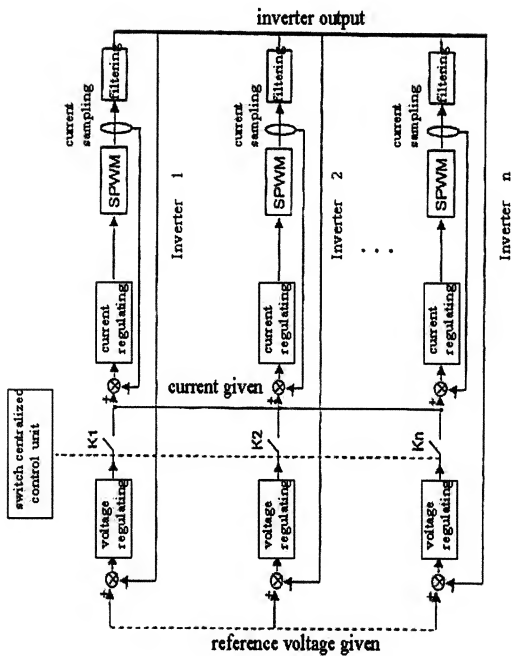


-Prior Art-

FIG. 1



-Prior Art-
 FIG. 2

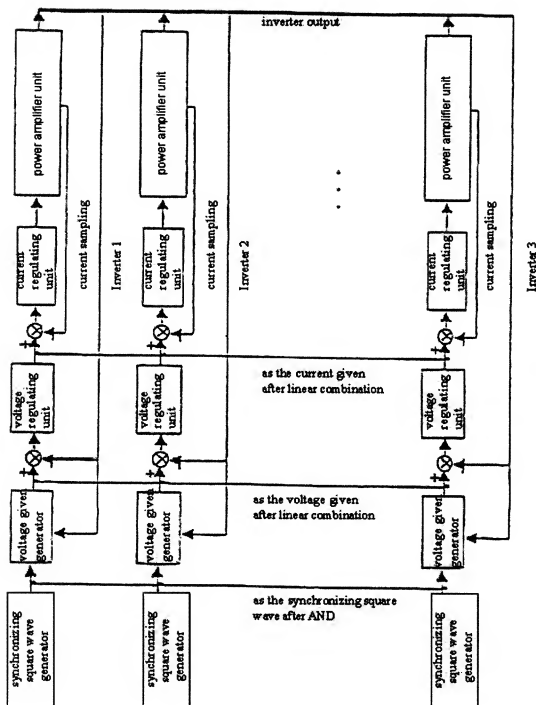


FIG. 3

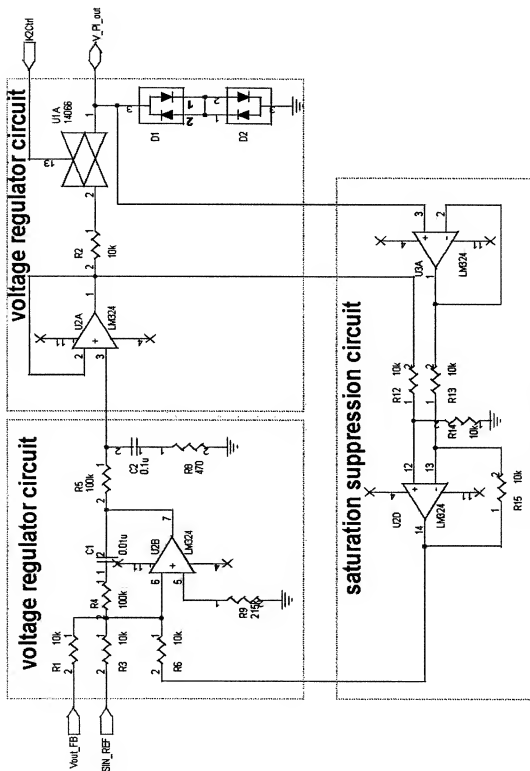
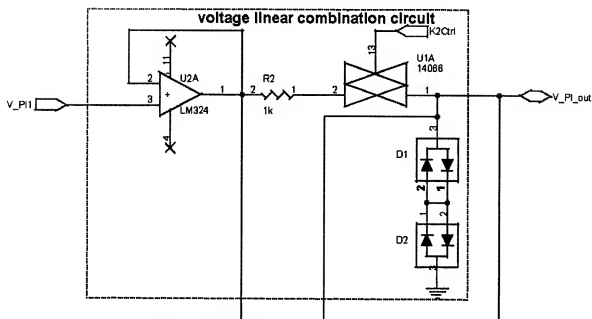
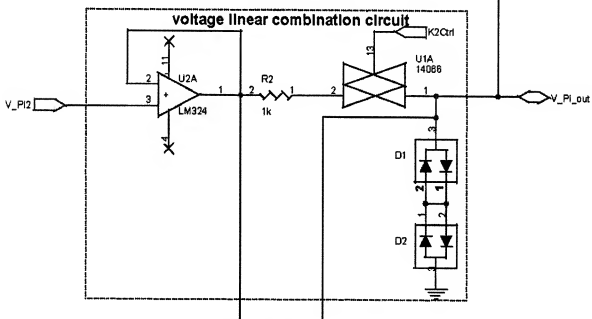


FIG. 7

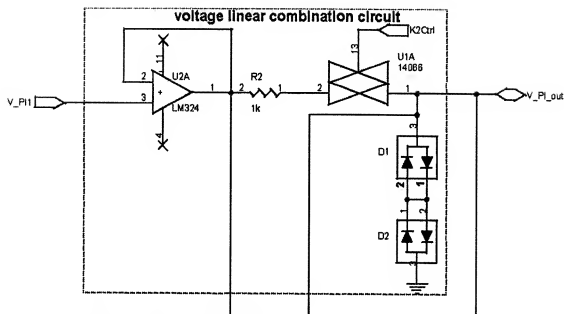


circuit in the module 1

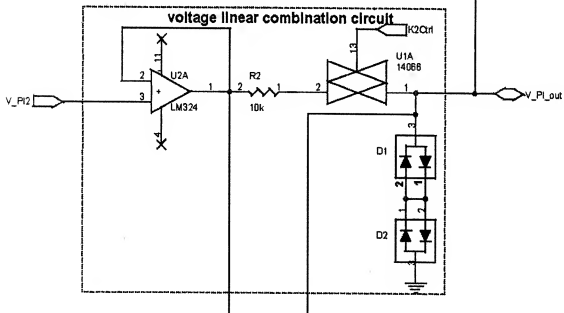


circuit in the module 2

FIG. 8



circuit in the module 1



circuit in the module 2

FIG. 9

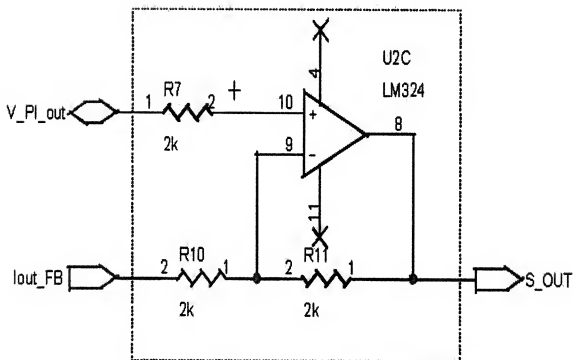


FIG. 10

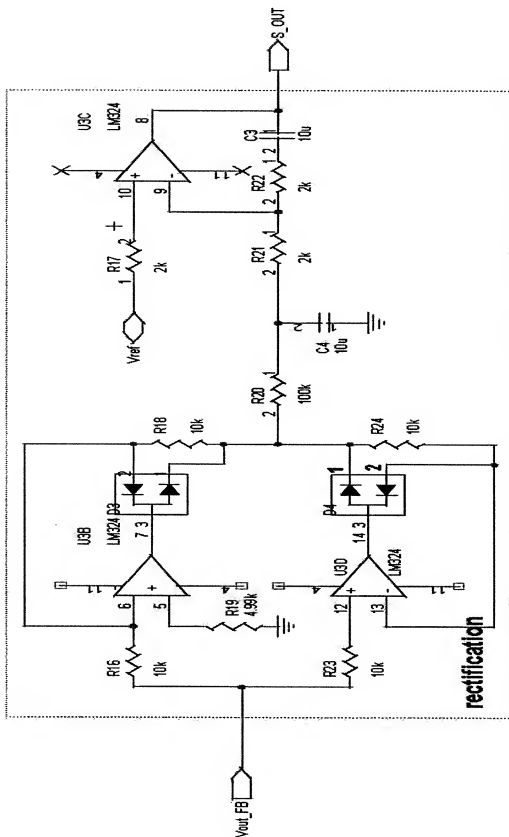


FIG. 11